EEL 4705

Survey on the Nano Logic Design Module

1. How did you find the lecture on Nano Logic in this class?
   a) Very Interesting
   b) Interesting
   c) Not interesting at all
   d) I don't care

Special Notes (if any):

2. Did you feel comfortable in extending the K-map knowledge and apply it to nanotechnology?
   a) Yes, very comfortable
   b) Comfortable
   c) Not comfortable at all
   d) I was not able to figure it out

Special Notes (if any):

3. How did you comprehend the lectures?

   a) I understood everything
   b) Understood quite a bit
   c) I think I know what was being lectured.
   d) Did not understand anything at all

Special Notes (if any):

4. How did you find the worksheet assignments on logic flow in QCA logic?

   a) It is very simple to understand and I was able to complete the worksheets without any problem.
   b) It was simple to understand the concept, but worksheets were hard to complete.
   c) It was difficult to understand the concept and to complete the worksheet.
   d) I was neither able to understand the concepts nor complete the worksheet.

Special Notes (if any):

5. Would you have liked to have more classes on different types of Nano logic devices?

   a) I feel it would be really interesting and fruitful.
   b) I think it would be interesting but I am not sure that it would be easy to understand.
   c) I feel it'll be more like a burden on us to understand and study.
   d) I don't think it is fruitful at all.

Special Notes (if any):

6. Do you think that these lectures were helpful in motivating you to study more on these devices?

   a) Yes, they motivated me
   b) I feel they were interesting and would be useful to me
   c) I do not think they were interesting or motivating
   d) I don't care.

Special Notes (if any):

7. Do you feel that Nano logic should be made a part of curriculum for future Logic design classes?

   a) I feel very strongly.
   b) I think it would be interesting
   c) Maybe
   d) I am against it.

Special Notes (if any):

8. Do you have any suggestions regarding the lecture on Nano logic?